

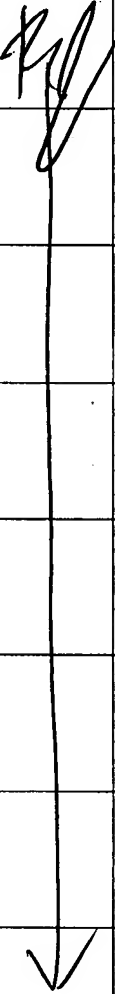
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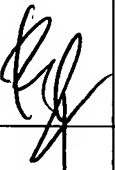

Application Number	10675831
Filing Date	2003-09-30
First Named Inventor	DeWitt, Jr. et al.
Art Unit	2183
Examiner Name	Johnson, Brian P.
Attorney Docket Number	AUS920030479US1

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	1	6542985		2003-04-01	Johnson et al.	
	2	4316245		1982-02-16	Luu et al.	
	3	7181723		2007-02-20	Luk et al.	
	4	6961925		2005-11-01	Callahan, II et al.	
	5	5950003		1999-09-30	Kaneshiro et al.	
	6	5212794		1993-05-18	Pettis et al.	
	7	5659679		1997-08-19	Alpert et al.	
	8	6006033		1999-12-21	Heisch	

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	9	6119075		2000-09-12	Dean et al.	
	10	6189141	B1	2001-02-13	Benitez et al.	
	11	6237141	B1	2001-05-22	Holzle et al.	
	12	6351844	B1	2002-02-26	Bala	
	13	6505292	B1	2003-01-07	Witt	
	14	6631514	B1	2003-10-07	Le	
	15	5794028		1998-08-11	Tran	
	16	5950009		1999-09-07	Bortnikov et al.	
	17	5950003		1999-09-07	Kaneshiro et al.	
	18	5581778		1996-12-03	Chin et al.	
	19	5752062		1998-05-12	Gover et al.	

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	20	6446029	B1	2002-09-03	Davidson et al.	
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	1	20040194076	A1	2004-09-01	Comp et al.	
	2	20020073406	A1	2002-06-13	Gove	
	3	20030014741	A1	2003-01-16	Megiddo et al.	
	4	20020157086	A1	2002-10-24	Lewis et al.	
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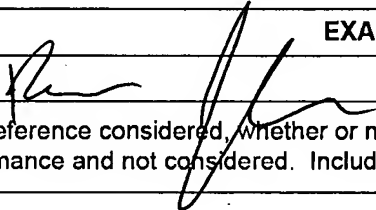
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	2	YANG et al., "Improving Performance by Branch Reordering", Proceedings of the ACM SIGPLAN 1998 Conference on Programming Language Design and Implementation, Montreal Canada, 1008, pp. 130-141.	<input type="checkbox"/>
	3	CONTE et al., "Accurate and Practical Profile-Driven Compilation Using the Profile Buffer", Proceedings of the 29th Annual ACM/IEEE International Symposium on Microarchitecture, Paris, France, 1996, pp. 36-45.	<input type="checkbox"/>
	4	CONTE et al., "Using Branch Handling Hardware to Support Profile-Driven Optimization", Proceedings of the 27th Annual International Symposium on Microarchitecture, San Jose CA, 1994, pp. 12-21.	<input type="checkbox"/>
	5	FISHER, "Trace Scheduling: A Technique for Global Microcode Compaction", IEEE Transactions on Computers, Vol. C30, No. 7, July 1981, pp. 478-490.	<input type="checkbox"/>
	6	CHANG et al., "Using Profile Information to Assist Classic Code Optimizations", Software Pract. Exper. 21, December 1991, pp. 1301-1321.	<input type="checkbox"/>
	7	SCHMIDT et al., "Profile-Directed Restructuring of Operating System Code", IBM Systems Journal, 1998, Vol. 37, No. 2, pp. 270-297.	<input type="checkbox"/>
	8	AHO et al., "Compilers: Principles, Techniques, and Tools", Addison-Wesley, 1988, pp. 488-497.	<input type="checkbox"/>
	9	INTEL, "Intel IA-64 Architecture Software Developer's Manual", Revision 1.1, Vol. 4, No. 245320.002, July 2001.	<input type="checkbox"/>

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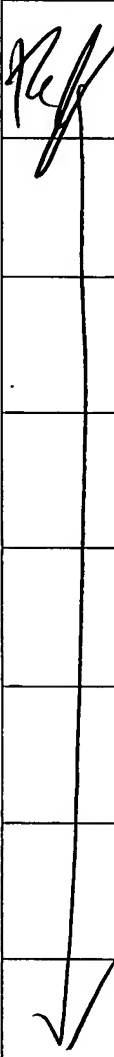
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	2	6928521	B1	2005-08-09	Burton et al.	
	3	6598153	B1	2003-07-22	Flachs et al.	
	4	5875294		1999-02-23	Roth et al.	
	5	6073109		2000-06-06	Flores et al.	
	6	6871298	B1	2005-03-22	Cavanaugh et al.	
	7	5555432		1996-09-10	Hinton et al.	
	8	6145077		2000-11-07	Sidwell et al.	

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	9	6865666	B2	2005-03-08	Yoshida et al.	
	10	5590352		1996-12-31	Zuraski et al.	
	11	6199204	B1	2001-03-06	Donohue	
	12	6202207	B1	2001-03-13	Donohue	

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	2	20050177822		2005-08-22	Kuch et al.	
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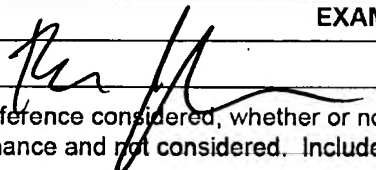
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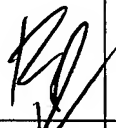

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	2	5991708		1999-11-23	Levine et al.	
	3	5564015		1996-10-08	Bunnell	
	4	5450349		1995-09-12	Brown et al.	
	5	5987598		1999-11-16	Levine et al.	
	6	6009514		1999-12-18	Henzinger et al.	
	7	6112317		2000-08-29	Berc et al.	
	8	5896538		1999-04-20	Blandy et al.	

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	Examiner Name		Johnson, Brian P.	
	Attorney Docket Number		AUS920030479US1	

	9	5355487		1994-10-11	Keller et al.	
	10	6948032	B2	2005-09-20	Kadambi et al.	

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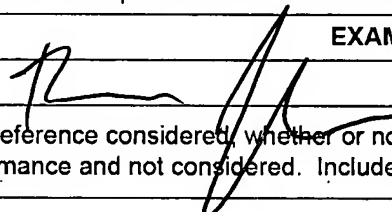
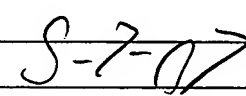
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	2	AMMONS et al., "Exploiting Hardware Performance Counters with Flow and Context Sensitive Profiling", 1997, ACM, pp. 85-96.	<input type="checkbox"/>

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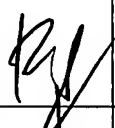

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	9	6973542	B1	2005-12-06	Schmuck et al.	
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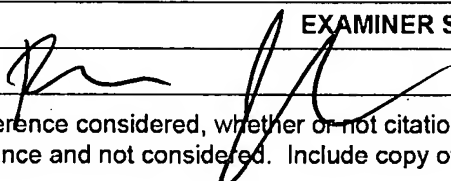
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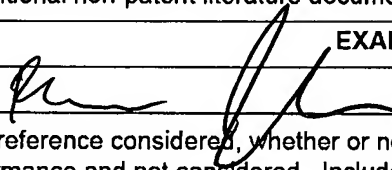
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Application Number	10675831
Filing Date	2003-09-30
First Named Inventor	DeWitt, Jr. et al.
Art Unit	2183
Examiner Name	Johnson, Brian P.
Attorney Docket Number	AUS920030479US1

Examiner Initials*	Cite No	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc), date, pages(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>5</sup>
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Application Number	10/675,831
Filing Date	09/30/2003
First Named Inventor	DeWitt, Jr. et al.
Examiner Name	Cody, Dillon J.
Art Unit	2183
Attorney Docket No.	AUS920030479US1

Examiner Initials	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
AA	6,149,318	11-21-2000	Chase et al.	
AB	6,185,671 B1	02-06-2001	Pentovski et al.	
AC	6,247,113 B1	06-12-2001	Jaggar	
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AE	2003/0126590 A1	07-03-2003	Burrows et al.	
AF	6,842,850 B2	01-11-2005	Ganapathy et al.	

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Examiner Initial	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

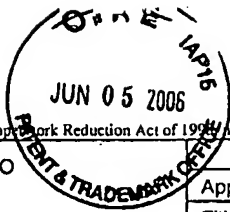
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AG	MERTEN et al., "A Hardware Driven Profiling Scheme for Identifying Program Hot Spots to Support Runtime Optimization", IEEE, 1999, pp. 136-147.			

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Examiner Initials	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
AA	5805879	09-08-1998	Hervin et al.	
AB	6647301 B2	11-11-2003	Sederlund et al.	
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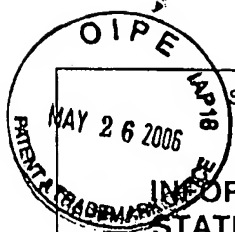
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Examiner Initials	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
AA	5051944	09-24-1991	Fetterolf et al.	
AB	5142634	08-25-1992	Fite et al.	
AC	5394529	02-28-1995	Brown, III et al.	
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AL	JEONG et al., "Cost Sensitive Cache Replacement Algorithms", June 2002, Second Workshop on Caching, Coherence and Consistency, NY, pp. 1-14.

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